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NOTICE OF ALLOWANCE AND FEE(S) DUE

57960

7590

08/05/2010

PVF -- ORACLE AMERICA, INC. C/O PARK, VAUGHAN & FLEMING LLP 2820 FIFTH STREET DAVIS, CA 95618-7759

EXAMINER				
CHEN, QING				
ART UNIT PAPER NUMBER				
2191				

DATE MAILED: 08/05/2010

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/646,309	08/22/2003	Gregory M. Wright	SUN-P9042	9198

TITLE OF INVENTION: REDUCING THE OVERHEAD INVOLVED IN EXECUTING NATIVE CODE IN A VIRTUAL MACHINE THROUGH

BINARY REOPTIMIZATION

APPLN. TYPE	SMALL ENTITY	ISSUE FEE DUE	PUBLICATION FEE DUE	PREV. PAID ISSUE FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1510	\$0	\$0	\$1510	11/05/2010

THE APPLICATION IDENTIFIED ABOVE HAS BEEN EXAMINED AND IS ALLOWED FOR ISSUANCE AS A PATENT. PROSECUTION ON THE MERITS IS CLOSED. THIS NOTICE OF ALLOWANCE IS NOT A GRANT OF PATENT RIGHTS. THIS APPLICATION IS SUBJECT TO WITHDRAWAL FROM ISSUE AT THE INITIATIVE OF THE OFFICE OR UPON PETITION BY THE APPLICANT. SEE 37 CFR 1.313 AND MPEP 1308.

THE ISSUE FEE AND PUBLICATION FEE (IF REQUIRED) MUST BE PAID WITHIN THREE MONTHS FROM THE MAILING DATE OF THIS NOTICE OR THIS APPLICATION SHALL BE REGARDED AS ABANDONED. THIS STATUTORY PERIOD CANNOT BE EXTENDED. SEE 35 U.S.C. 151. THE ISSUE FEE DUE INDICATED ABOVE DOES NOT REFLECT A CREDIT FOR ANY PREVIOUSLY PAID ISSUE FEE IN THIS APPLICATION. IF AN ISSUE FEE HAS PREVIOUSLY BEEN PAID IN THIS APPLICATION (AS SHOWN ABOVE), THE RETURN OF PART B OF THIS FORM WILL BE CONSIDERED A REQUEST TO REAPPLY THE PREVIOUSLY PAID ISSUE FEE TOWARD THE ISSUE FEE NOW DUE.

HOW TO REPLY TO THIS NOTICE:

I. Review the SMALL ENTITY status shown above.

If the SMALL ENTITY is shown as YES, verify your current SMALL ENTITY status:

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B. If the status above is to be removed, check box 5b on Part B - Fee(s) Transmittal and pay the PUBLICATION FEE (if required) and twice the amount of the ISSUE FEE shown above, or

If the SMALL ENTITY is shown as NO:

A. Pay TOTAL FEE(S) DUE shown above, or

B. If applicant claimed SMALL ENTITY status before, or is now claiming SMALL ENTITY status, check box 5a on Part B - Fee(s) Transmittal and pay the PUBLICATION FEE (if required) and 1/2 the ISSUE FEE shown above.

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III. All communications regarding this application must give the application number. Please direct all communications prior to issuance to Mail Stop ISSUE FEE unless advised to the contrary.

IMPORTANT REMINDER: Utility patents issuing on applications filed on or after Dec. 12, 1980 may require payment of maintenance fees. It is patentee's responsibility to ensure timely payment of maintenance fees when due.

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Complete and send this form, together with applicable fee(s), to: Mail Mail Stop ISSUE FEE

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appropriate. All further indicated unless correcte maintenance fee notifica	correspondence including ed below or directed other tions.	ng the Patent, advance onerwise in Block 1, by (rders and notification of a) specifying a new corre	maintenance fees espondence address	will be ; and/or	mailed to the current (b) indicating a sepa	correspondence address as rate "FEE ADDRESS" for
CURRENT CORRESPONDENCE ADDRESS (Note: Use Block 1 for any change of address) 57960 7590 08/05/2010 PVF ORACLE AMERICA, INC. C/O PARK, VAUGHAN & FLEMING LLP 2820 FIFTH STREET			Fe pa	e(s) Transmittal. Thoers. Each addition	iis certii al paper	icate cannot be used for	r domestic mailings of the or any other accompanying nt or formal drawing, must
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							(Signature)
			L				(Date)
APPLICATION NO.	FILING DATE		FIRST NAMED INVENTO	R	ATTO	RNEY DOCKET NO.	CONFIRMATION NO.
10/646,309	08/22/2003	•	Gregory M. Wright		•	SUN-P9042	9198
TITLE OF INVENTION BINARY REOPTIMIZA		OVERHEAD INVOLVE	D IN EXECUTING NA	TIVE CODE IN .	A VIRT	TUAL MACHINE TH	IROUGH
APPLN. TYPE	SMALL ENTITY	ISSUE FEE DUE	PUBLICATION FEE DUE	PREV. PAID ISSU	JE FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1510	\$0	\$0		\$1510	11/05/2010
EXAM	IINER	ART UNIT	CLASS-SUBCLASS				
CHEN,	QING	2191	717-157000				
 Change of correspondence address or indication of "Fee Address" (37 CFR 1.363). Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached. "Fee Address" indication (or "Fee Address" Indication form PTO/SB/47; Rev 03-02 or more recent) attached. Use of a Customer Number is required. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON 			or agents OR, alternat (2) the name of a sing registered attorney or 2 registered patent att listed, no name will b	(1) the names of up to 3 registered patent attorneys or agents OR, alternatively, (2) the name of a single firm (having as a member a registered attorney or agent) and the names of up to 2 registered patent attorneys or agents. If no name is listed, no name will be printed. E PATENT (print or type)			
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Please check the appropr	rate assignee category or	categories (will not be pr	rinted on the patent):	■ Individual 🔲 C	orporati	on or other private gro	up entity 🗖 Government
4a. The following fee(s): Issue Fee	are submitted:	4	 b. Payment of Fee(s): (Ple A check is enclosed. 	ease first reapply a	ny prev	iously paid issue fee s	shown above)
	No small entity discount p	permitted)	A check is enclosed. Payment by credit card. Form PTO-2038 is attached.				
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5. Change in Entity Sta	*	*					
	s SMALL ENTITY state d Publication Fee (if rec		b. Applicant is no lo	-			e assignee or other party in
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10/646,309	08/22/2003 Gregory M. Wright		SUN-P9042	9198	
57960 75	90 08/05/2010		EXAM	INER	
PVF ORACL	E AMERICA, INC.		CHEN,	QING	
,	GHAN & FLEMING L	LP	ART UNIT	PAPER NUMBER	
2820 FIFTH STREET DAVIS, CA 95618-7759			2191		
DA VIS, CA 75016	-1137		DATE MAILED: 08/05/2010		

Determination of Patent Term Adjustment under 35 U.S.C. 154 (b)

(application filed on or after May 29, 2000)

The Patent Term Adjustment to date is 944 day(s). If the issue fee is paid on the date that is three months after the mailing date of this notice and the patent issues on the Tuesday before the date that is 28 weeks (six and a half months) after the mailing date of this notice, the Patent Term Adjustment will be 944 day(s).

If a Continued Prosecution Application (CPA) was filed in the above-identified application, the filing date that determines Patent Term Adjustment is the filing date of the most recent CPA.

Applicant will be able to obtain more detailed information by accessing the Patent Application Information Retrieval (PAIR) WEB site (http://pair.uspto.gov).

Any questions regarding the Patent Term Extension or Adjustment determination should be directed to the Office of Patent Legal Administration at (571)-272-7702. Questions relating to issue and publication fee payments should be directed to the Customer Service Center of the Office of Patent Publication at 1-(888)-786-0101 (571)-272-4200.

	Application No.	Applicant(s)
A	10/646,309	WRIGHT ET AL.
Notice of Allowability	Examiner	Art Unit
	Qing Chen	2191
The MAILING DATE of this communication apperall claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RI of the Office or upon petition by the applicant. See 37 CFR 1.313	(OR REMAINS) CLOSED in this ap or other appropriate communication IGHTS. This application is subject to and MPEP 1308.	plication. If not included will be mailed in due course. THIS
	-	
2. The allowed claim(s) is/are <u>1,2,4-6,10,11,13-15 and 28-35</u> ,	<u>renumbered as 1-18</u> .	
 3. Acknowledgment is made of a claim for foreign priority ur a) All b) Some* c) None of the: 1. Certified copies of the priority documents have 	been received.	
2. Certified copies of the priority documents have	· · · —	
3. Copies of the certified copies of the priority do	cuments have been received in this	national stage application from the
International Bureau (PCT Rule 17.2(a)). * Certified copies not received:		
Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONM THIS THREE-MONTH PERIOD IS NOT EXTENDABLE. 4. A SUBSTITUTE OATH OR DECLARATION must be subm INFORMAL PATENT APPLICATION (PTO-152) which give by the Correct of Draftspers (a) including changes required by the Notice of Draftspers 1) hereto or 2) to Paper No./Mail Date (b) including changes required by the attached Examiner's Paper No./Mail Date Identifying indicia such as the application number (see 37 CFR 1. each sheet. Replacement sheet(s) should be labeled as such in the correct of th	itted. Note the attached EXAMINER es reason(s) why the oath or declarant be submitted. Son's Patent Drawing Review (PTO- Son's Amendment / Comment or in the Comment of the drawing he header according to 37 CFR 1.121(sit of BIOLOGICAL MATERIAL resistance.	'S AMENDMENT or NOTICE OF ation is deficient. 948) attached Office action of a line in the front (not the back) of d). must be submitted. Note the
Attachment(s) 1. ☐ Notice of References Cited (PTO-892) 2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948) 3. ☐ Information Disclosure Statements (PTO/SB/08), Paper No./Mail Date 4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material	5. ☐ Notice of Informal F 6. ☐ Interview Summary Paper No./Mail Da 7. ⊠ Examiner's Amendr	Patent Application (PTO-413), te

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DETAILED ACTION

1. This Office action is in response to the amendment filed on May 17, 2010.

- 2. Claims 1, 2, 4-6, 10, 11, 13-15, and 28-35 are pending.
- 3. Claims 1, 2, 4-6, 9-11, 13-15, 18, 28, 29, 31-33, and 35 have been amended.
- 4. Claims 3, 7-9, 12, 16-27, and 36-39 have been canceled.
- 5. **Claims 1, 2, 4-6, 10, 11, 13-15, and 28-35** are allowed, renumbered as 1-18.
- 6. The objections to Claims 2, 9, 11, and 18 are withdrawn in view of Applicant's amendments to the claims or Examiner's amendments to the claims.
- 7. The 35 U.S.C. § 112, second paragraph, rejections of Claims 32-35 are withdrawn in view of Applicant's amendments to the claims.

Examiner's Amendment

8. An Examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to Applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this Examiner's amendment was given in a telephone interview with Anthony Jones (Reg. No. 59,521) on July 27, 2010.

The application has been amended as follows:

AMENDMENTS TO THE CLAIMS

In the "Amendments to the Claims" (received on 05/17/2010), please cancel Claims 7-9 and 16-18 and amend Claims 1, 2, 6, 10, 11, 15, 28, 31, 32, and 35 as follows:

1. (Currently Amended) A method for reducing an overhead involved in executing native code methods in an application running on a virtual machine, comprising:

selecting a call to any native code method to be optimized within the virtual machine; decompiling at least part of the native code method for the selected call into an intermediate representation, wherein an intermediate representation includes a set of instruction code which is not in final executable form, wherein decompiling at least the part of the native code method involves setting up a context for a decompilation by determining a signature of the selected call and determining a mapping from arguments of the selected call to corresponding locations in a native application binary interface (ABI);

obtaining [[an]] a previously-generated intermediate representation associated with the application running on the virtual machine which interacts with the native code method for the selected call;

integrating the intermediate representation for the native code method for the selected call into the intermediate representation associated with the application running on the virtual machine to form an integrated intermediate representation; and

generating a native code from the integrated intermediate representation, wherein generating the native code from the integrated intermediate representation involves optimizing interactions between the application running on the virtual machine and the native code method for the selected call, wherein optimizing the interactions involves optimizing calls from the

application to the native code method for the selected call by using additional information from the integrated intermediate representation to reduce a number of indirect calls and indirect references associated with the calls from the application to the native code method for the selected call.

2. (Currently Amended) The method of claim 1, wherein selecting the call to any native code method involves selecting the call based upon at least one of:

an execution frequency of the selected call; and

an overhead involved in performing the <u>selected</u> call as compared against an amount of work performed by the native code method for the <u>selected</u> call.

6. (Currently Amended) The method of claim 4,

wherein the virtual machine is a platform-independent virtual machine; and wherein integrating the intermediate representation for the native code method for the selected call with the <u>previously-generated</u> intermediate representation associated with the application running on the virtual machine involves integrating calls provided by an interface for accessing native code into the native code method for the selected call.

7-9. (Canceled)

10. (Currently Amended) A computer-readable storage device storing instructions that when executed by a computer cause the computer to perform a method for reducing an overhead

involved in executing native code methods in an application running on a virtual machine, the method comprising:

selecting a call to any native code method to be optimized within the virtual machine; decompiling at least part of the native code method for the selected call into an intermediate representation, wherein an intermediate representation includes a set of instruction code which is not in final executable form, wherein decompiling at least the part of the native code method involves setting up a context for a decompilation by determining a signature of the selected call and determining a mapping from arguments of the selected call to corresponding locations in a native application binary interface (ABI);

obtaining [[an]] a previously-generated intermediate representation associated with the application running on the virtual machine which interacts with the native code method for the selected call;

integrating the intermediate representation for the native code method for the selected call into the intermediate representation associated with the application running on the virtual machine to form an integrated intermediate representation; and

generating a native code from the integrated intermediate representation, wherein generating the native code from the integrated intermediate representation involves optimizing interactions between the application running on the virtual machine and the native code method for the selected call, wherein optimizing the interactions involves optimizing calls from the application to the native code method for the selected call by using additional information from the integrated intermediate representation to reduce a number of indirect calls and indirect

references associated with the calls from the application to the native code method for the selected call.

11. (Currently Amended) The computer-readable storage device of claim 10, wherein selecting the call to any native code method involves selecting the call based upon at least one of:

an execution frequency of the selected call; and

an overhead involved in performing the <u>selected</u> call as compared against an amount of work performed by the native code method for the <u>selected</u> call.

15. (Currently Amended) The computer-readable storage device of claim 13, wherein the virtual machine is a platform-independent virtual machine; and wherein integrating the intermediate representation for the native code method for the selected call with the <u>previously-generated</u> intermediate representation associated with the application running on the virtual machine involves integrating calls provided by an interface for accessing native code into the native code method for the selected call.

16-18. (Canceled)

28. (Currently Amended) A method for reducing an overhead involved in executing native code methods in an application running on a virtual machine, comprising:

deciding to optimize a callback by any native code method into the virtual machine;

decompiling at least part of the native code method for the callback into an intermediate representation, wherein an intermediate representation includes a set of instruction code which is not in final executable form, wherein decompiling at least the part of the native code method involves setting up a context for a decompilation by determining a signature of the selected callback and determining a mapping from arguments of the selected callback to corresponding locations in a native application binary interface (ABI);

obtaining [[an]] <u>a previously-generated</u> intermediate representation associated with the application running on the virtual machine which interacts with the native code method for the callback;

integrating the intermediate representation for the native code method for the callback into the intermediate representation associated with the application running on the virtual machine to form an integrated intermediate representation; and

generating a native code from the integrated intermediate representation, wherein generating the native code from the integrated intermediate representation involves optimizing the callback, wherein optimizing the callback involves optimizing calls from the native code method for the callback to the application by using additional information from the integrated intermediate representation to reduce a number of indirect calls and indirect references associated with the calls from the native code method for the callback to the application.

31. (Currently Amended) The method of claim 28,

wherein the virtual machine is a platform-independent virtual machine; and

wherein integrating the intermediate representation for the native code method for the callback with the <u>previously-generated</u> intermediate representation associated with the application running on the virtual machine involves integrating calls provided by an interface for accessing native code into the native code method for the callback.

32. (Currently Amended) A computer-readable storage device storing instructions that when executed by a computer cause the computer to perform a method for reducing an overhead involved in executing native code methods in an application running on a virtual machine, the method comprising:

deciding to optimize a callback by any native code method into the virtual machine; decompiling at least part of the native code method for the callback into an intermediate representation, wherein an intermediate representation includes a set of instruction code which is not in final executable form, wherein decompiling at least the part of the native code method involves setting up a context for a decompilation by determining a signature of the selected callback and determining a mapping from arguments of the selected callback to corresponding locations in a native application binary interface (ABI);

obtaining [[an]] <u>a previously-generated</u> intermediate representation associated with the application running on the virtual machine which interacts with the native code method for the callback;

integrating the intermediate representation for the native code method for the callback into the intermediate representation associated with the application running on the virtual machine to form an integrated intermediate representation; and

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generating a native code from the integrated intermediate representation, wherein generating the native code from the integrated intermediate representation involves optimizing the callback, wherein optimizing the callback involves optimizing calls from the native code method for the callback to the application by using additional information from the integrated intermediate representation to reduce a number of indirect calls and indirect references associated with the calls from the native code method for the callback to the application.

35. (Currently Amended) The computer-readable storage device of claim 32, wherein the virtual machine is a platform-independent virtual machine; and wherein integrating the intermediate representation for the native code method for the callback with the <u>previously-generated</u> intermediate representation associated with the application running on the virtual machine involves integrating calls provided by an interface for accessing native code into the native code method for the callback.

-- END OF AMENDMENT --

Reasons for Allowance

9. The following is an Examiner's statement of reasons for allowance:

The cited prior art taken alone or in combination fail to teach, in combination with the other claimed limitations, "obtaining a previously-generated intermediate representation associated with the application running on the virtual machine which interacts with the native code method for the selected call; integrating the intermediate representation for the native code

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method for the selected call into the intermediate representation associated with the application running on the virtual machine to form an integrated intermediate representation; and generating a native code from the integrated intermediate representation, wherein generating the native code from the integrated intermediate representation involves optimizing interactions between the application running on the virtual machine and the native code method for the selected call, wherein optimizing the interactions involves optimizing calls from the application to the native code method for the selected call by using additional information from the integrated intermediate representation to reduce a number of indirect calls and indirect references associated with the calls from the application to the native code method for the selected call" as recited in independent Claims 1 and 10; and further fail to teach, in combination with the other claimed limitations, "obtaining a previously-generated intermediate representation associated with the application running on the virtual machine which interacts with the native code method for the callback; integrating the intermediate representation for the native code method for the callback into the intermediate representation associated with the application running on the virtual machine to form an integrated intermediate representation; and generating a native code from the integrated intermediate representation, wherein generating the native code from the integrated intermediate representation involves optimizing the callback, wherein optimizing the callback involves optimizing calls from the native code method for the callback to the application by using additional information from the integrated intermediate representation to reduce a number of indirect calls and indirect references associated with the calls from the native code method for the callback to the application" as recited in independent Claims 28 and 32.

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The closest cited prior art, the combination of US 6,289,506 (hereinafter "Kwong") and US 6,412,109 (hereinafter "Ghosh"), teaches a method for optimizing Java performance using precompiled code. However, the combination of Kwong and Ghosh fails to teach "obtaining a previously-generated intermediate representation associated with the application running on the virtual machine which interacts with the native code method for the selected call; integrating the intermediate representation for the native code method for the selected call into the intermediate representation associated with the application running on the virtual machine to form an integrated intermediate representation; and generating a native code from the integrated intermediate representation, wherein generating the native code from the integrated intermediate representation involves optimizing interactions between the application running on the virtual machine and the native code method for the selected call, wherein optimizing the interactions involves optimizing calls from the application to the native code method for the selected call by using additional information from the integrated intermediate representation to reduce a number of indirect calls and indirect references associated with the calls from the application to the native code method for the selected call" as recited in independent Claims 1 and 10; and further fails to teach "obtaining a previously-generated intermediate representation associated with the application running on the virtual machine which interacts with the native code method for the callback; integrating the intermediate representation for the native code method for the callback into the intermediate representation associated with the application running on the virtual machine to form an integrated intermediate representation; and generating a native code from the integrated intermediate representation, wherein generating the native code from the integrated intermediate representation involves optimizing the callback, wherein optimizing the callback

involves optimizing calls from the native code method for the callback to the application by using additional information from the integrated intermediate representation to reduce a number of indirect calls and indirect references associated with the calls from the native code method for the callback to the application" as recited in independent Claims 28 and 32.

Any comments considered necessary by Applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

10. Any inquiry concerning this communication or earlier communications from the Examiner should be directed to Qing Chen whose telephone number is 571-270-1071. The Examiner can normally be reached on Monday through Thursday from 7:30 AM to 4:00 PM. The Examiner can also be reached on alternate Fridays.

If attempts to reach the Examiner by telephone are unsuccessful, the Examiner's supervisor, Wei Zhen, can be reached on 571-272-3708. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the TC 2100 Group receptionist whose telephone number is 571-272-2100.

Information regarding the status of an application may be obtained from the Patent
Application Information Retrieval (PAIR) system. Status information for published applications
may be obtained from either Private PAIR or Public PAIR. Status information for unpublished

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applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

/Q. C./

Examiner, Art Unit 2191

/Wei Y Zhen/

Supervisory Patent Examiner, Art Unit 2191